

## WEST Search History

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DATE: Thursday, June 24, 2004

Hide?	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
		<i>DB=USPT,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>	
<input type="checkbox"/>	L5	L2 same (processor near3 memory)	8
<input type="checkbox"/>	L4	L2 same (memory or cache)	29
<input type="checkbox"/>	L3	L2 same memory	29
<input type="checkbox"/>	L2	L1 same (((low\$5 or reduc\$4) adj power\$4) or (power\$4 adj down) or sleep or standby)	42
<input type="checkbox"/>	L1	(sav\$4 near2 process\$4 near2 state)	612

END OF SEARCH HISTORY

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L Number	Hits	Search Text	DB	Time stamp
-	564	(power\$3 near3 cache) same (processor or cpu)	USPAT; US-PGPUB	2004/06/23 12:56
-	74	((power\$3 near3 cache) same (processor or cpu)) and 713/3\$.ccls.	USPAT; US-PGPUB	2004/06/23 10:59
-	49	((power\$3 near3 cache) same (processor or cpu)) and 713/32\$.ccls.	USPAT; US-PGPUB	2004/06/23 11:03
-	3	((second adj1 voltage) near3 cache) same (processor or cpu)	USPAT; US-PGPUB	2004/06/23 12:54
-	372	((suppl\$3 near3 (voltage or power)) near3 cache) same (processor or cpu)	USPAT; US-PGPUB	2004/06/23 11:02
-	6	((suppl\$3 adj1 (voltage or power)) near3 cache) same (processor or cpu)	USPAT; US-PGPUB	2004/06/23 11:02
-	3	((suppl\$3 near3 (voltage or power)) near3 cache) same (processor or cpu)) and 713/32\$.ccls.	USPAT; US-PGPUB	2004/06/23 11:03
-	11	((suppl\$3 near3 (voltage or power)) near3 cache) same (processor or cpu)) and 713/3\$.ccls.	USPAT; US-PGPUB	2004/06/23 11:03
-	569	(second adj1 cache) same (processor or cpu)	USPAT; US-PGPUB	2004/06/23 15:48
-	6	((second adj1 cache) same (processor or cpu)) and 713/32\$.ccls.	USPAT; US-PGPUB	2004/06/23 12:54
-	26	((power\$3 adj1 down) near3 cache) same (processor or cpu)	USPAT; US-PGPUB	2004/06/23 14:45
-	396	(back\$1up or backup) near3 cache	USPAT; US-PGPUB	2004/06/23 13:11
-	3	((back\$1up or backup) near3 cache) and 713/32\$.ccls.	USPAT; US-PGPUB	2004/06/23 13:03
-	97	((back\$1up or backup) near3 cache) with (processor or cpu)	USPAT; US-PGPUB	2004/06/23 13:11
-	7	((battery) near3 cache) with (processor or cpu)	USPAT; US-PGPUB	2004/06/23 13:14
-	0	((auxilliary) near3 cache) with (processor or cpu)	USPAT; US-PGPUB	2004/06/23 13:15
-	32	((aux\$8) near3 cache) with (processor or cpu)	USPAT; US-PGPUB	2004/06/23 13:20
-	1	((aux\$8 adj2 power) with cache) with (processor or cpu)	USPAT; US-PGPUB	2004/06/23 13:17
-	5	((battery near3 (backup or back-up)) near3 cache) with (processor or cpu)	USPAT; US-PGPUB	2004/06/23 13:20
-	408	(second adj1 cache) with (processor or cpu)	USPAT; US-PGPUB	2004/06/23 13:22
-	0	(second adj1 cache) with(processor or cpu) with battery	USPAT; US-PGPUB	2004/06/23 13:22
-	2	(second adj1 cache) with(processor or cpu) with voltage	USPAT; US-PGPUB	2004/06/23 13:22
-	0	((second adj1 cache) with (processor or cpu)) with battery	USPAT; US-PGPUB	2004/06/23 13:42
-	0	(integrated near3 (back-up or backup adj2 cache)) same (processor or cpu)	USPAT; US-PGPUB	2004/06/23 13:43
-	52	(integrated near3 (back-up or backup adj2 cache))	USPAT; US-PGPUB	2004/06/23 13:44
-	0	(integrated near3 ((back-up or backup) adj2 cache))	USPAT; US-PGPUB	2004/06/23 13:45
-	85	(second adj1 voltage) near5 (input\$3 or suppl\$4) with (processor or cpu)	USPAT; US-PGPUB	2004/06/23 13:46
-	7	((second adj1 voltage) near5 (input\$3 or suppl\$4) with (processor or cpu)) and 713/32\$.ccls.	USPAT; US-PGPUB	2004/06/23 13:49
-	3	((stor\$3 or sav\$3) near3 context) with processor with (sleep or standby or stand-by)	USPAT; US-PGPUB	2004/06/23 13:56
-	49	(cache) with processor with (sleep or standby or stand-by)	USPAT; US-PGPUB	2004/06/23 13:52
-	17	((cache) with processor with (sleep or standby or stand-by)) and 713/3\$.ccls.	USPAT; US-PGPUB	2004/06/23 13:52
-	34	((stor\$3 or sav\$3) near3 state) with processor with (sleep or standby or stand-by)	USPAT; US-PGPUB	2004/06/23 13:57

-	25	((stor\$3 or sav\$3) near3 (processor or cpu) near3 state) with (sleep or standby or stand-by)	USPAT; US-PGPUB	2004/06/23 13:59
-	2	(power\$3 near2 cache) with (processor or cpu) with (sleep or standby or stand-by)	USPAT; US-PGPUB	2004/06/23 14:22
-	2476	(processor or cpu) with (integrated near2 memory)	USPAT; US-PGPUB	2004/06/23 14:23
-	10	((processor or cpu) with (integrated near2 memory)) and 713/32\$.ccls.	USPAT; US-PGPUB	2004/06/23 14:23
-	1	((constant\$3 or always) near3 power\$3 near3 cache) same (processor or cpu)	USPAT; US-PGPUB	2004/06/23 14:50
-	1139	(internal near2 cache) with (cpu or processor)	USPAT; US-PGPUB	2004/06/23 14:51
-	9	((internal near2 cache) with (cpu or processor)) with (sleep or standby or stand-by or (low adj1 power) or low-power)	USPAT; US-PGPUB	2004/06/23 15:27
-	10	((separat\$4 near2 power\$3) near6 (cache)) same (processor or cpu)	USPAT; US-PGPUB	2004/06/23 15:28
-	26	((separat\$4 near2 power\$3) near6 (cache))	USPAT; US-PGPUB	2004/06/23 15:30
-	1	6738675.pn.	USPAT; US-PGPUB	2004/06/23 15:30
-	17	("5021679"   "5153535"   "5307003"   "5513358"   "5625806"   "5627412"   "5752011"   "5813022"   "5963023"   "5974557"   "6108226"   "6141765"   "6216235"   "6298105"   "6347379"   "6351150"   "6487668").PN.	USPAT	2004/06/23 15:30
-	11	5627412.URPN.	USPAT	2004/06/23 15:32
-	43	(second adj1 cache) same (processor or cpu)	EPO; JPO; IBM TDB	2004/06/23 15:49
-	3	5918056.pn. 5784628.pn. 5652890.pn.	USPAT	2004/06/24 14:07